

# Abstracts

## A Technique for Minimizing Intermodulation Distortion of GaAs FET's

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*H. Koizumi, S. Nagata and K. Kanazawa. "A Technique for Minimizing Intermodulation Distortion of GaAs FET's." 1995 Transactions on Microwave Theory and Techniques 43.2 (Feb. 1995 [T-MTT]): 463-466.*

This paper describes the theory to minimize the intermodulation distortion under certain current bias condition for GaAs FET's. A device-parametric study has been done to obtain the general equation that provides the lowest distortion condition as a function of an operating current. Based on the present theory, FET parameters have been designed practically.

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